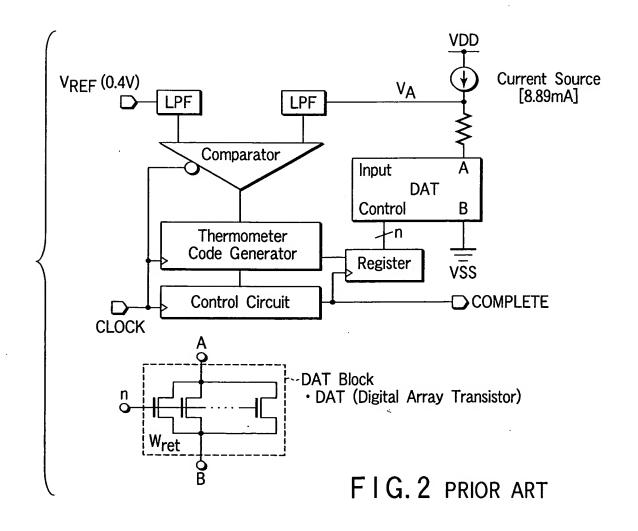


FIG. 1 PRIOR ART



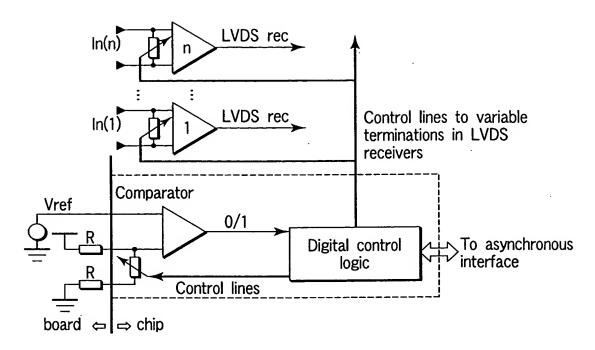


FIG. 3 PRIOR ART

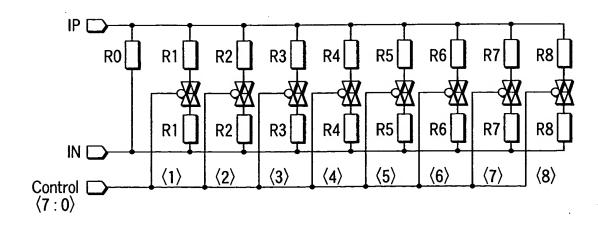


FIG. 4 PRIOR ART

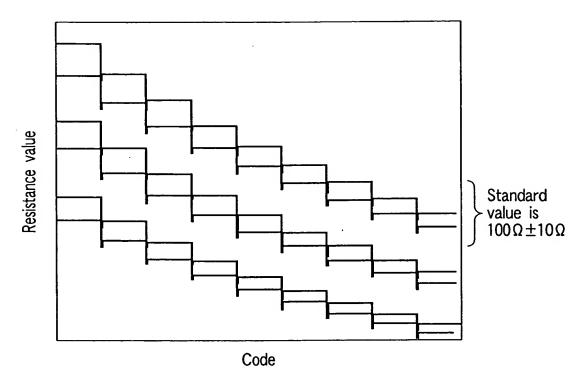


FIG. 5 PRIOR ART

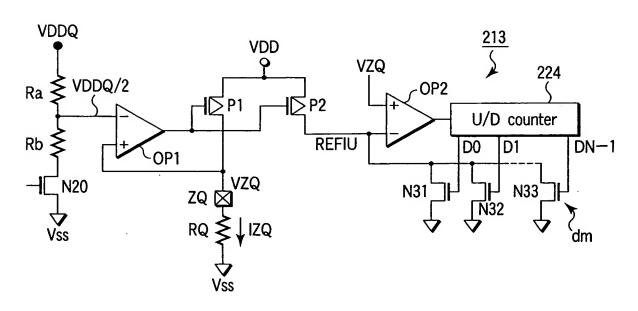
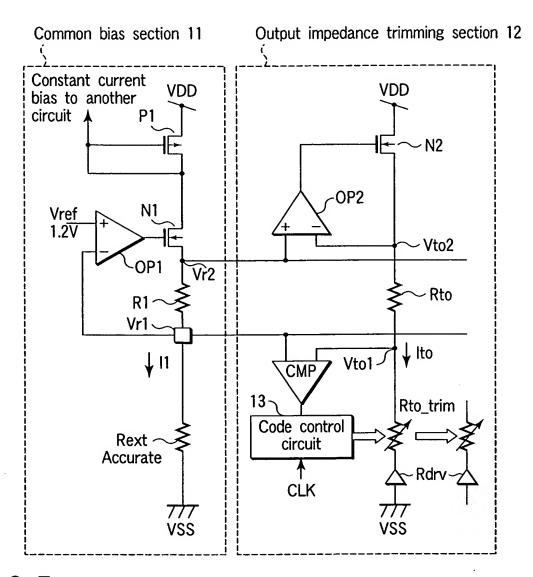
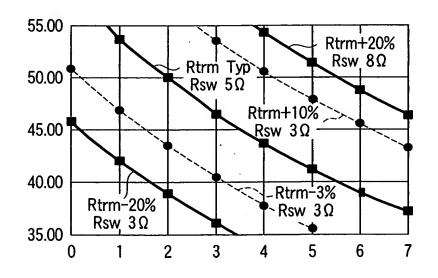


FIG. 6 PRIOR ART



F I G. 7

F1G.8



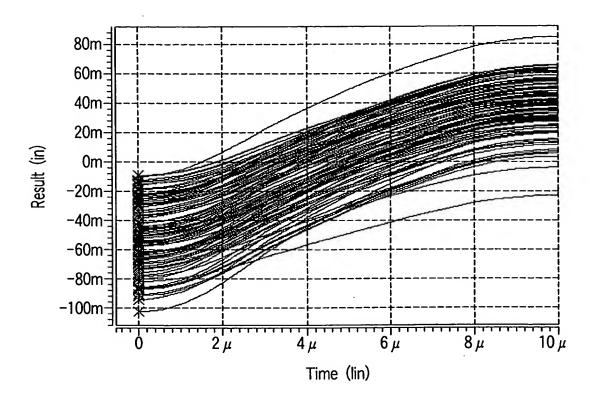


FIG. 9

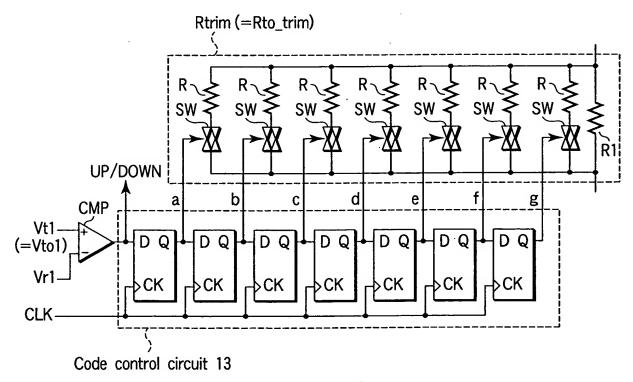
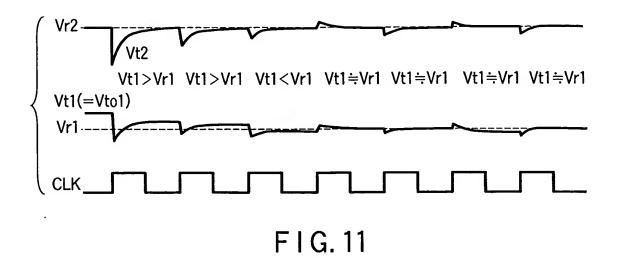
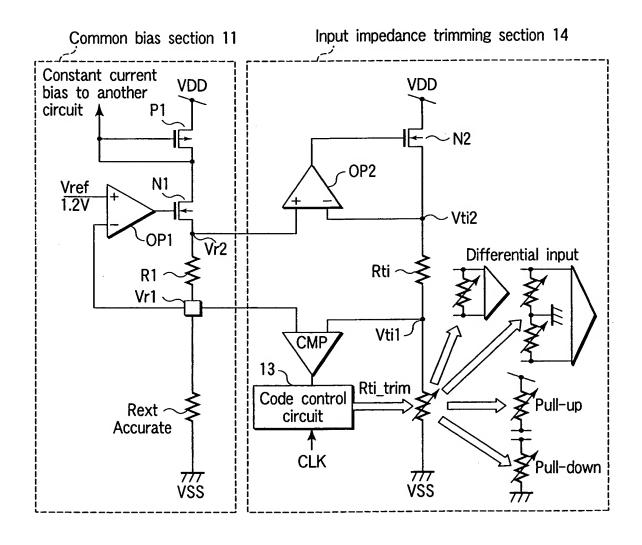
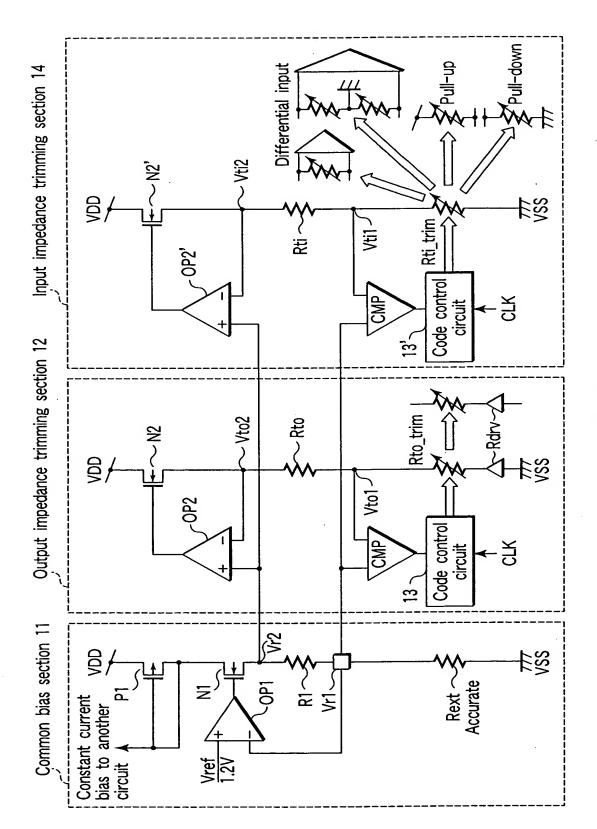


FIG. 10





F I G. 12



F1G. 13

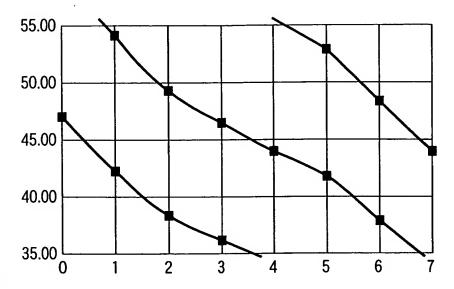
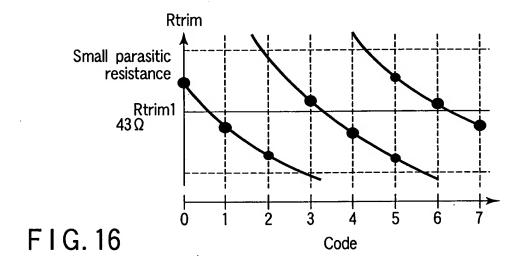
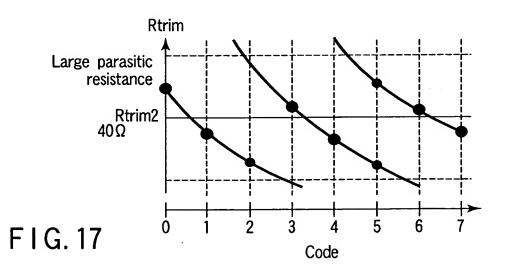
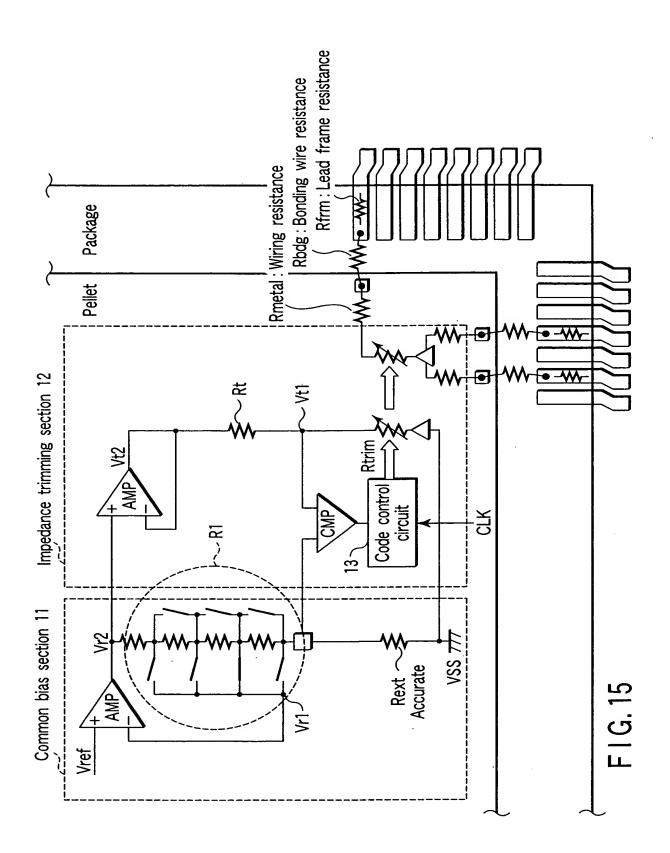


FIG. 14







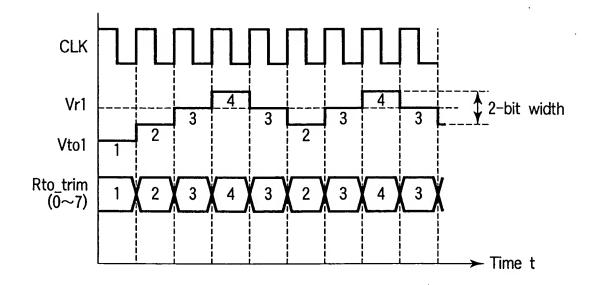


FIG. 18

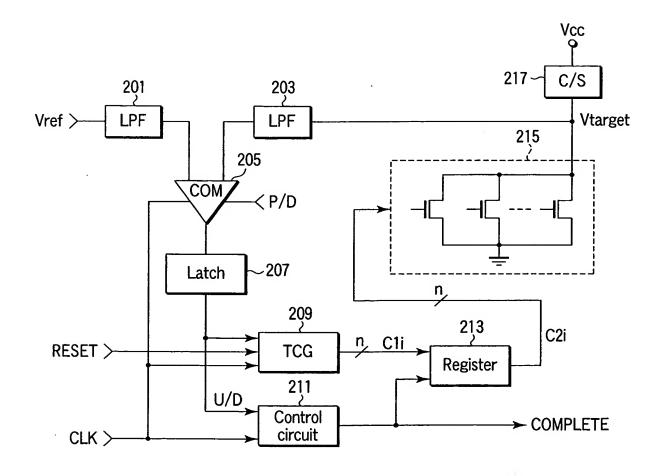


FIG. 19

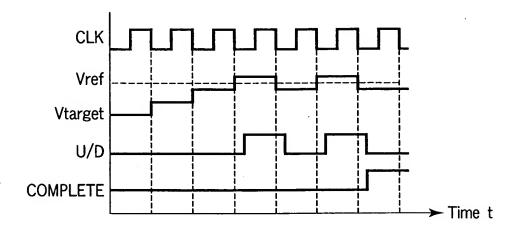
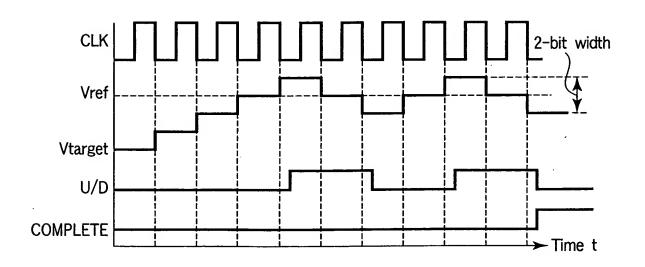


FIG. 20



F I G. 21

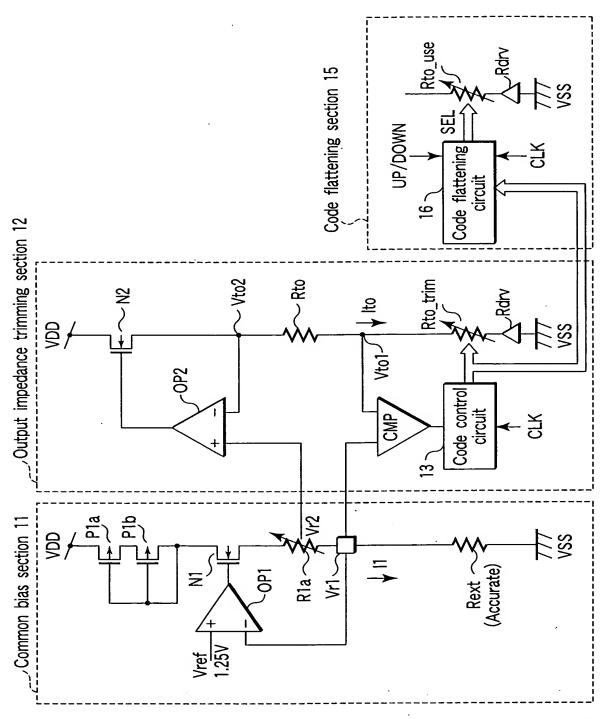
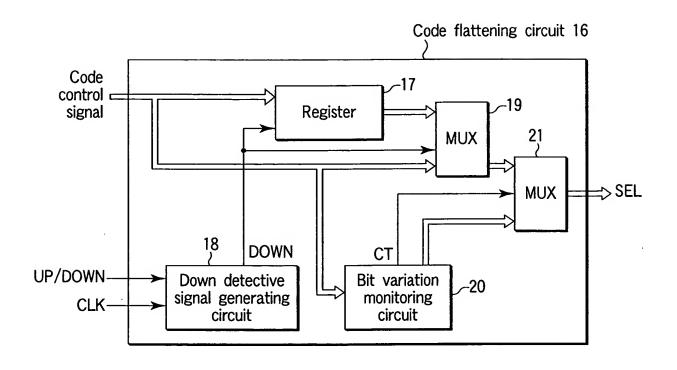


FIG. 22



F I G. 23



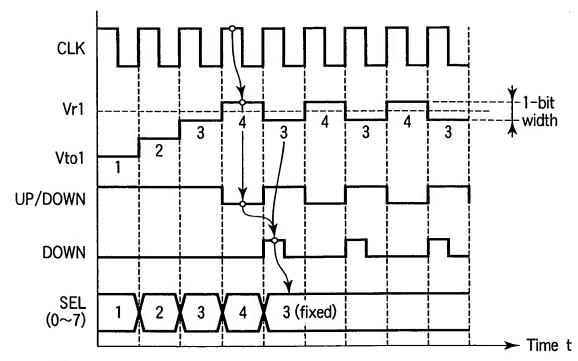


FIG. 24

When Vto1 varies by 2-bit width

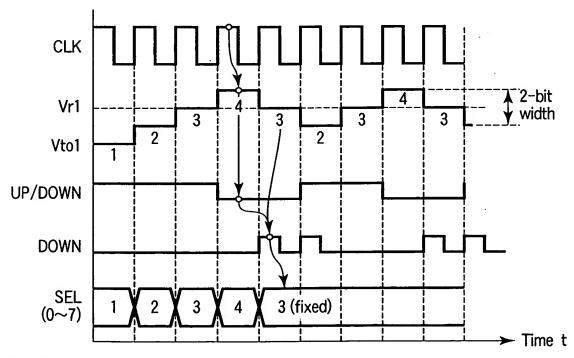


FIG. 25



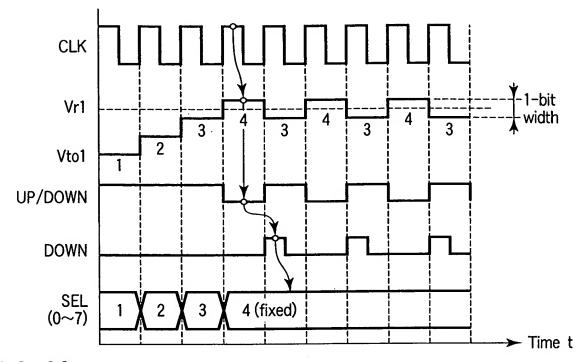


FIG. 26



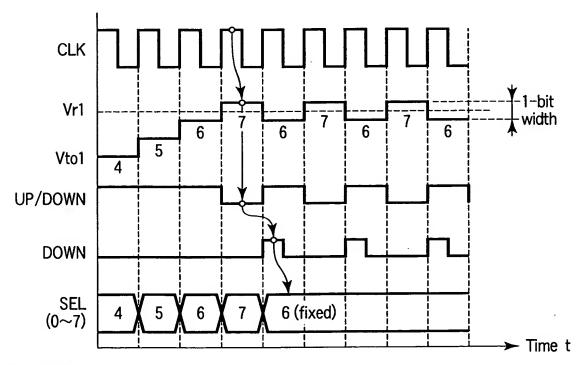
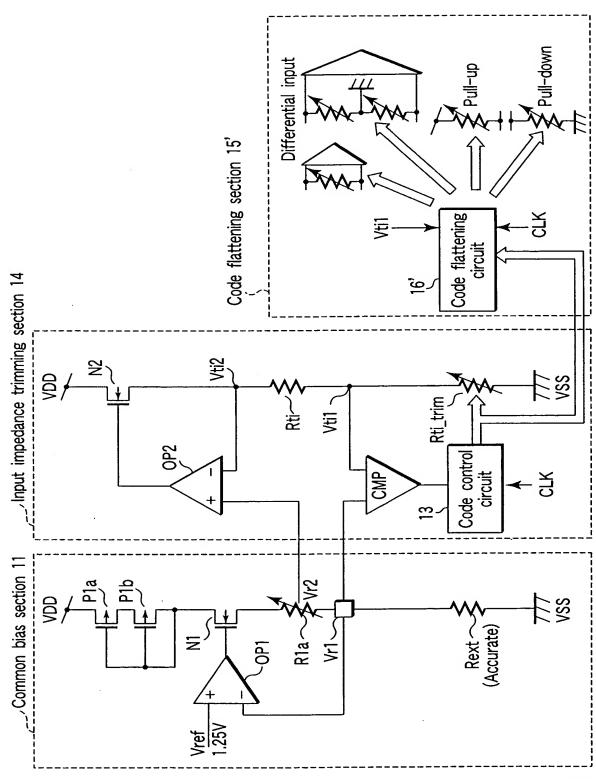
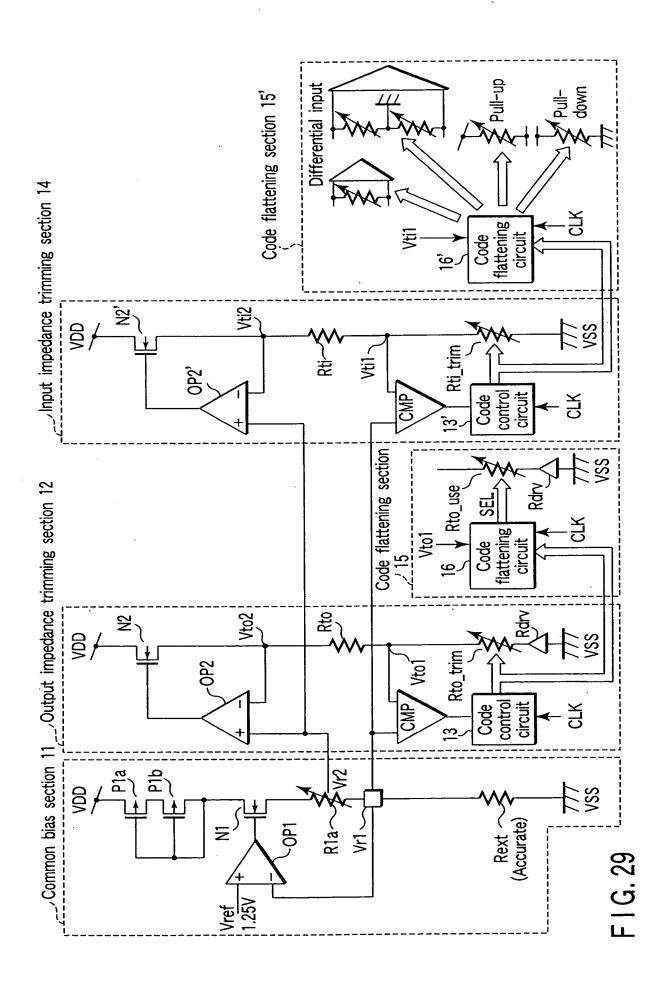


FIG. 27



F1G.28



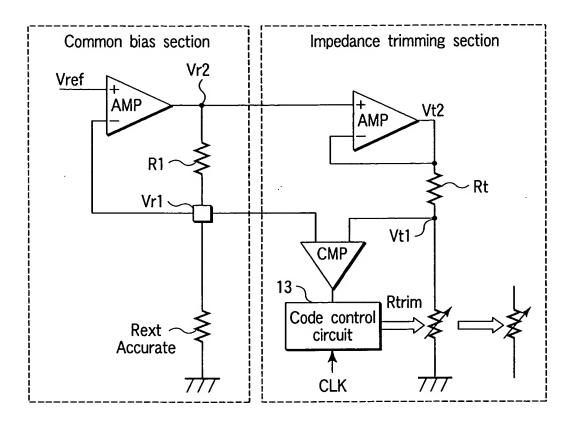


FIG. 30

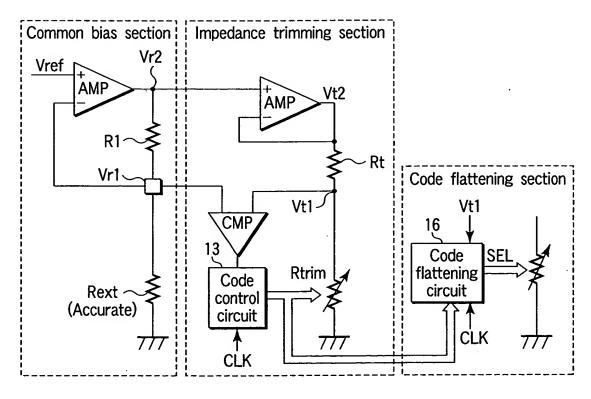


FIG. 31